

Fig. 2

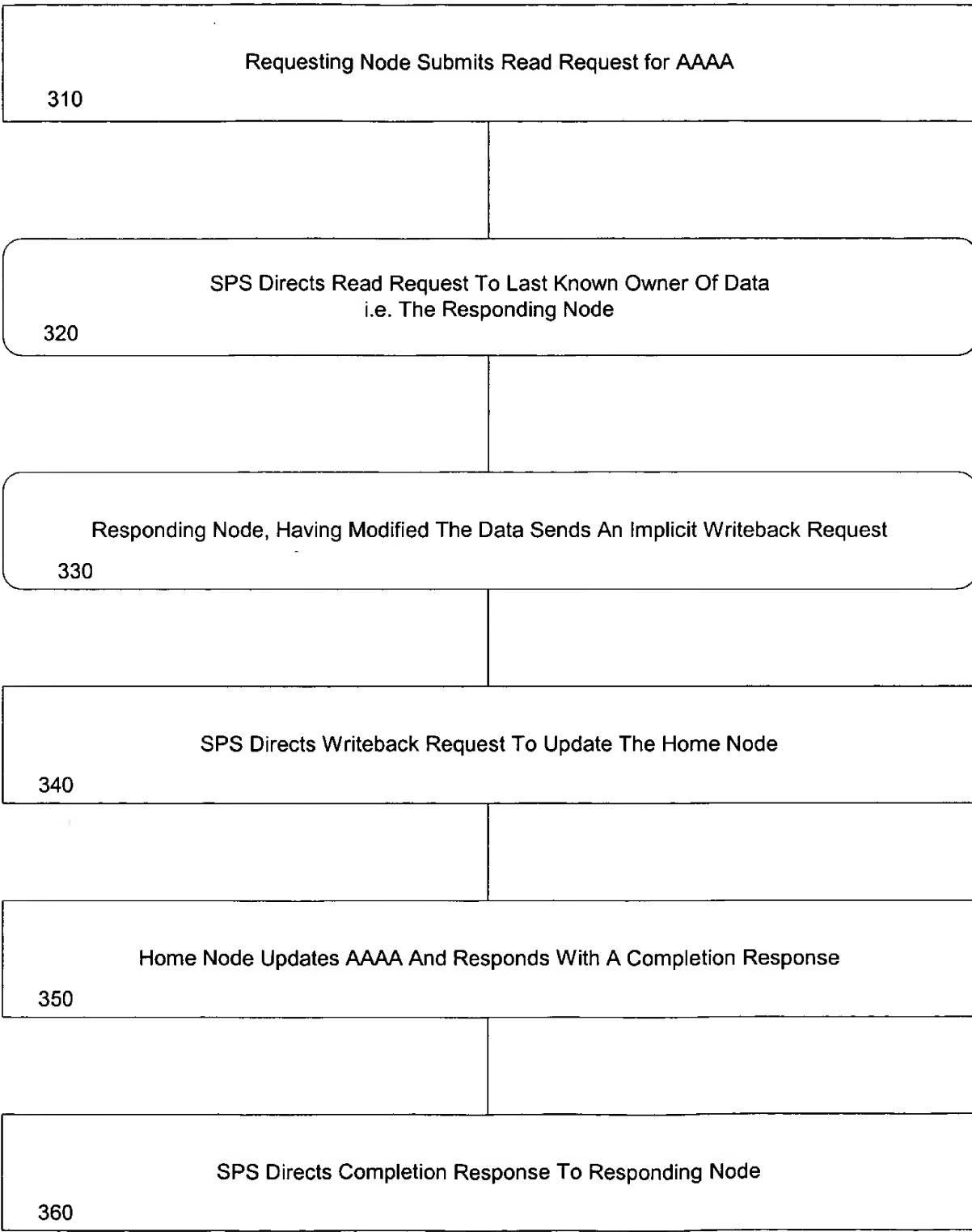


Fig. 3

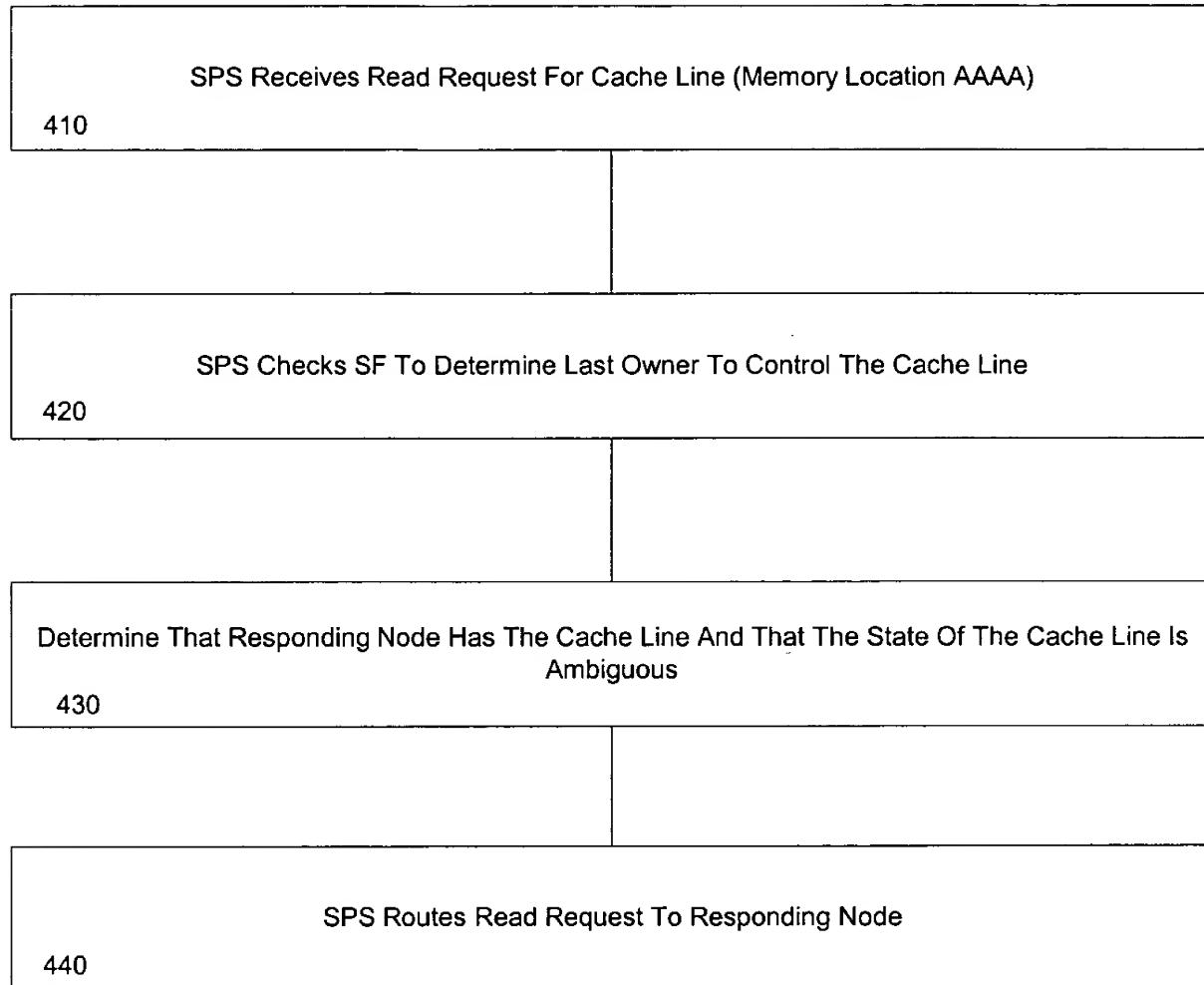


Fig. 4

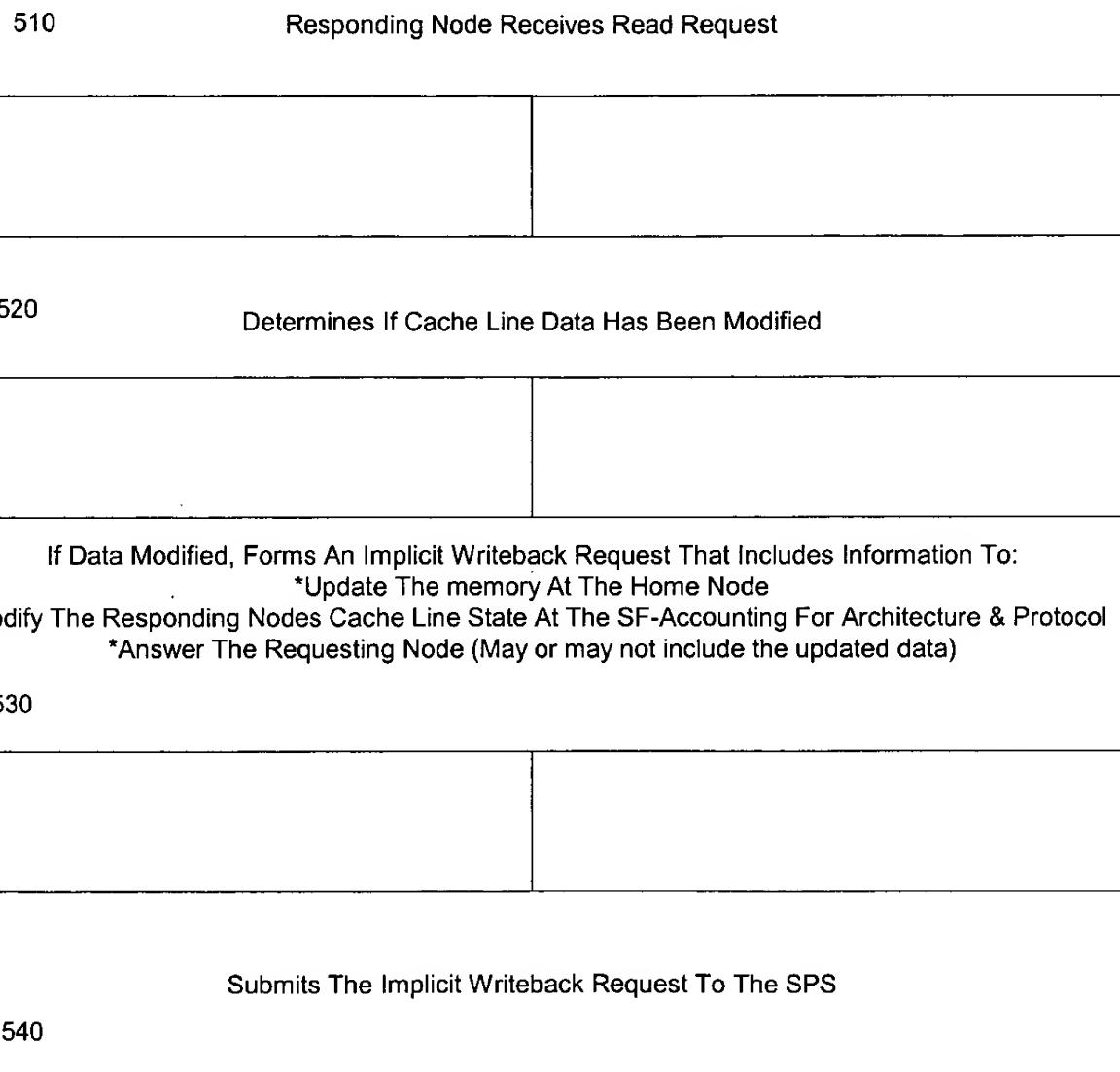


Fig. 5

| | 14 Bits | 4 Bits | 2 Bits | 20 Bits |
|----------|--------------------|---|-------------|----------|
| phit (0) | Reserved | Resp Type [3:0] | Route [1:0] | Reserved |
| 610 | | | | |
| 611 | Route Bits | Conveyed Meaning | | |
| 612 | [1] [0] | provide data to requesting node update home node memory "i" implicit writeback | | |
| 613 | | | | |
| 614 | Response Type Bits | Conveyed Meaning | | |
| | [3:2] [1:0] | type of response; "11" = snoop response state; "10" M S; "11" = M S | | |

Fig. 6

Request

IA32

IOH

Route [1:0]

| | | | | |
|----------|----------------|----------------|----------------|----------------|
| | | | | |
| 10 | PRLC/PRLD | PSNRM | PSNRMS | PSNRM |
| 20 30 | PSIL PSILND | PSNRM PSNRM | PSNRM PSNRM | PSNRM PSNRM |
| 40 | PSFCL | PSNRM | PSNRM | PSNRM |

Fig. 7

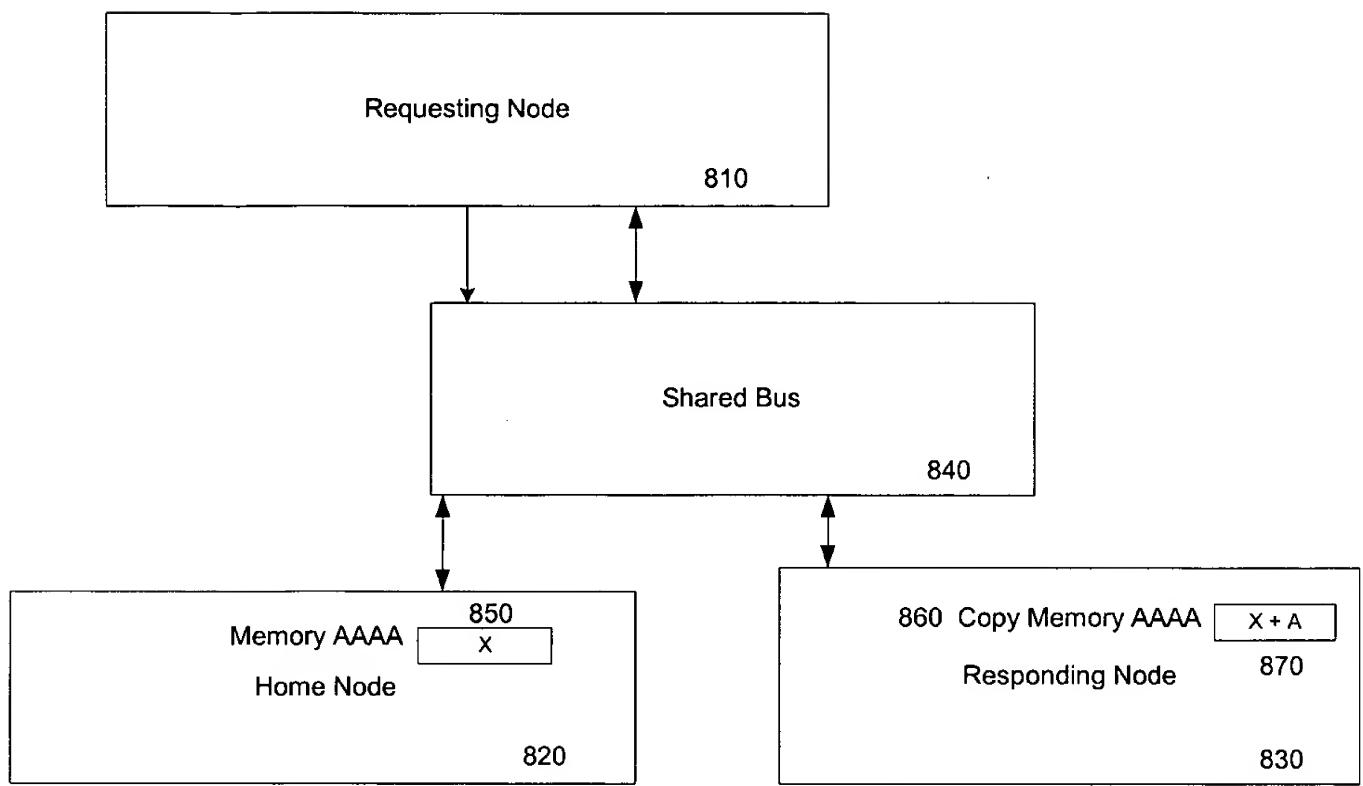


Fig. 8

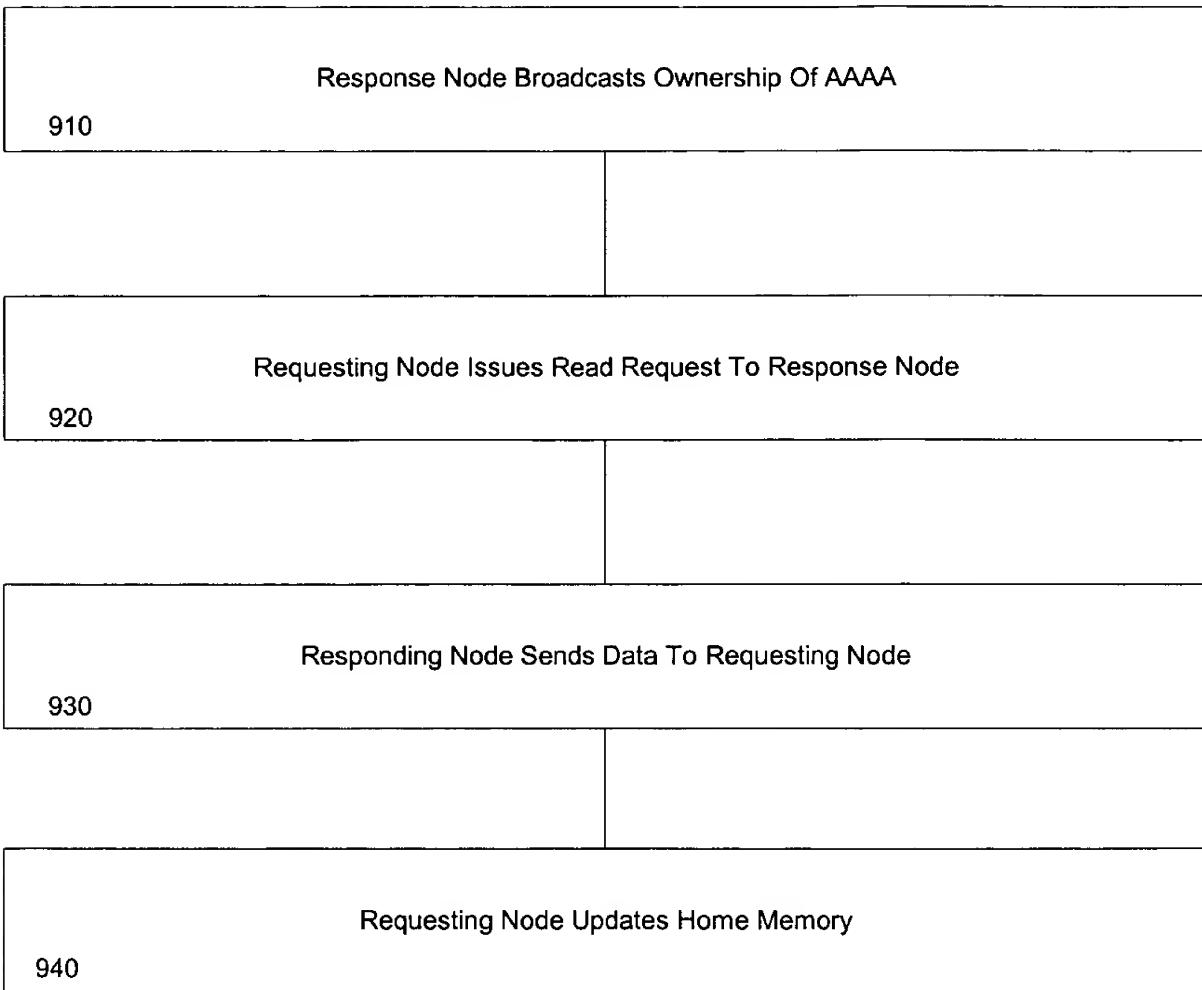


Fig. 9